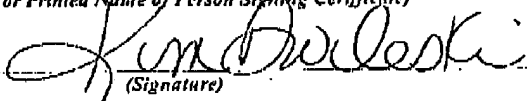


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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)		Docket No. BUR920040024US1	
Applicant(s): Bernstein et al.			
Application No. 10/710,453	Filing Date 7/13/2004	Examiner Vibol Tan	Group Art Unit 2819
Invention: LOW LEAKAGE MONOTONIC CMOS LOGIC			
<p>I hereby certify that this <u>Request for Reconsideration (21 pages)</u> <small>(Identify type of correspondence)</small></p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>571-273-8300</u>)</p> <p>on <u>3/9/2006 3/9/06</u> <small>(Date)</small></p> <div style="text-align: right; margin-top: 100px;"> <p>Kim Dwileski <small>(Typed or Printed Name of Person Signing Certificate)</small></p>  <small>(Signature)</small> </div>			
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/710,453

Applicant(s): Bernstein et al.

Filed.: July 13, 2004

Art Unit: 2819

Dkt. No.: BUR920040024US1

Examiner: Vibol Tan

Title: **LOW LEAKAGE MONOTONIC CMOS LOGIC**

Honorable Commissioner for Patents

P. O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR RECONSIDERATION

This Request for Reconsideration is being filed in response to the Office Action mailed on December 20, 2005.

BUR920040024US1